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PATENT DOCUMENTS								
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Information Disclosure Statement-PTO 1449 (modified)

SERIAL NO. 10/008,270 Form PTO-1449 (moderate ATTY. DKT. NO. 5181-96200 List of Patents and Publications For Applicant's Information 17 10% APPLICANT: Cavanagh, et al. **GROUP: 2123** Disclosure Statement (Use several sheets if necessal FILING DATE: November 9, 2001 **U.S. PATENT DOCUMENTS** FILING DATE IF **CLASS** SUB DATE NAME REF. **DOCUMENT NUMBER** EXAM. APPROPRIATE **CLASS** INITIALS_ DES. 10/99 Robbins, et al. 5,973,638 11/95 Ulrich, et al. 5,466,200 Bunza, Geoffrey J. 11/98 5,838,948 9/01 Stallmo, et al. 6,289,398 Gessel, et al. 3/99 5,889,954 Widdoes, Jr., L. Curtis 4,590,581 5/86 5,247,650 9/93 Judd, et al. Cheriton, David R. 5,893,155 4/99 FOREIGN PATENT DOCUMENTS TRANSLATION CLASS COUNTRY SUB EXAM. REF. DOCUMENT NUMBER DATE YES/NO **CLASS INITIALS** DES. OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Damani, Om, et al., "Fault-Tolerant Distributed Simulation," May 1998, Univ. Texas, 9 pages. D1 Tay, Seng Chuan, et al., "Speculative Parallel Simulation with an Adaptive Throttle Sceme, 1997, IEEE, pages D2 116-123. Lee, K.C., "A Virtual Bus Architecture for Dynamic Parallel Processing Parallel and Distributed Systems," D3IEEE Transactions, Vol. 4, Issue 2, February 1993, pages 121-130. Lee, K.C., "A Virtual Bus for Dynamic Parallel Processing Parallel and Distributed Processing, 1990, D4 Proceedings of the Second IEEE Symposium on December 9-13, 1990, pages 736-743. DATE CONSIDERED: \ 2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

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